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Abstract. The main goal of this book is to enable Electronic System Level (ESL) research based on an open-source infrastructure. Two key components in this infrastructure are SystemC, as a hardware and system description language, and ArchC, as an architecture description language.

[Automatic Architecture Description Language\(ADL\)-Based ...](#)

The current level of circuit integration led to complex designs encompassing full systems on a single chip, known as System-on-a-Chip (SoC). In order to predict the best design options and reduce

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This chapter presents a guideline to help designing new processors with ArchC. Instead of covering the syntax and semantics of the language, as Chap. 2 does, we will focus on the design flow of a